



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**  
HYDERABAD - 500 085, TELANGANA STATE, INDIA.

**CONSOLIDATED MARKS MEMO / CREDIT SHEET**

B.Tech. ELECTRONICS & COMMUNICATION ENGINEERING

CMM. No. : **C 0797710**

Serial No. : 21225023121

Name : **TOLETI SAI SATYA PRAKASH**



Name of the College : WJ-G N I T C, KHANAPUR

Month & Year of Final Exam : May, 2017

Hall Ticket No. : **13WJ1A04AZ**

Year of Admission : 2013-2014

Class Awarded : **FIRST CLASS WITH DISTINCTION**

S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS	S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS
<b>I YEAR</b>											
1	ENGLISH	22	50	72	4	2	MATHEMATICS - I	18	26	44	6
3	MATHEMATICAL METHODS	19	26	45	6	4	ENGINEERING PHYSICS	20	29	49	6
5	ENGINEERING CHEMISTRY	20	50	70	6	6	COMPUTER PROGRAMMING	19	26	45	6
7	ENGINEERING DRAWING	21	26	47	6	8	COMPUTER PROGRAMMING LAB.	23	45	68	4
9	ENGINEERING PHYSICS & ENGINEERING CHEMISTRY LA	23	43	66	4	10	ENGLISH LANGUAGE COMMUNICATION SKILLS LAB.	25	49	74	4
11	ENGINEERING WORKSHOP / IT WORKSHOP	25	49	74	4						
<b>I SEMESTER</b>											
<b>II YEAR</b>						<b>II SEMESTER</b>					
1	MATHEMATICS - III	16	29	45	4	1	PRINCIPLES OF ELECTRICAL ENGINEERING	19	66	85	4
2	PROBABILITY THEORY AND STOCHASTIC PROCESSES	22	46	68	4	2	ELECTRONIC CIRCUIT ANALYSIS	22	37	59	4
3	SWITCHING THEORY AND LOGIC DESIGN	23	39	62	4	3	PULSE AND DIGITAL CIRCUITS	22	46	68	4
4	ELECTRICAL CIRCUITS	24	26	50	4	4	ENVIRONMENTAL STUDIES	21	65	86	4
5	ELECTRONIC DEVICES & CIRCUITS	19	38	57	4	5	ELECTROMAGNETIC THEORY AND TRANSMISSION LIN	22	32	54	4
6	SIGNALS AND SYSTEMS	16	43	59	4	6	DIGITAL DESIGN USING VERILOG HDL	24	70	94	4
7	ELECTRONIC DEVICES AND CIRCUITS LAB	25	49	74	2	7	ELECTRICAL TECHNOLOGY LAB	24	49	73	2
8	BASIC SIMULATION LAB	24	40	64	2	8	ELECTRONIC CIRCUITS AND PULSE CIRCUITS LAB	25	45	70	2
<b>I SEMESTER</b>											
<b>III YEAR</b>						<b>II SEMESTER</b>					
1	CONTROL SYSTEMS ENGINEERING	21	32	53	4	1	MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS	22	30	52	4
2	COMPUTER ORGANIZATION AND OPERATING SYSTEMS	24	33	57	4	2	INTELLECTUAL PROPERTY RIGHTS	23	40	63	4
3	ANTENNAS AND WAVE PROPAGATION	19	50	69	4	3	DIGITAL COMMUNICATIONS	23	44	67	4
4	ELECTRONIC MEASUREMENTS AND INSTRUMENTATION	22	55	77	4	4	VLSI DESIGN	22	26	48	4
5	ANALOG COMMUNICATIONS	23	45	68	4	5	MICROPROCESSORS AND MICROCONTROLLERS	19	44	63	4
6	LINEAR AND DIGITAL IC APPLICATIONS	15	52	67	4	6	DIGITAL SIGNAL PROCESSING	20	32	52	4
7	ANALOG COMMUNICATIONS LAB	24	45	69	2	7	MICROPROCESSORS AND MICROCONTROLLERS LAB	24	46	70	2
8	IC APPLICATIONS AND HDL SIMULATION LAB	23	49	72	2	8	DIGITAL SIGNAL PROCESSING LAB	22	48	70	2
<b>I SEMESTER</b>											
<b>IV YEAR</b>						<b>II SEMESTER</b>					
1	MANAGEMENT SCIENCE	23	31	54	4	1	SATELLITE COMMUNICATIONS	21	53	74	4
2	MICROWAVE ENGINEERING	18	26	44*	4	2	RADAR SYSTEMS	22	50	72	4
3	COMPUTER NETWORKS	21	53	74	4	3	WIRELESS COMMUNICATIONS AND NETWORKS	22	31	53	4
4	CELLULAR AND MOBILE COMMUNICATIONS	25	35	60	4	4	INDUSTRY ORIENTED MINI PROJECT	-	42	42	2
5	DIGITAL IMAGE PROCESSING	22	27	49	4	5	SEMINAR	46	-	46	2
6	EMBEDDED SYSTEMS DESIGN	17	26	43*	4	6	MAJOR PROJECT	50	140	190	10
7	ADVANCED COMMUNICATION SKILLS LAB	25	41	66	2	7	COMPREHENSIVE VIVA	-	95	95	2
8	MICROWAVE ENGINEERING AND DIGITAL COMMUNICATI	20	47	67	2						
(# Project Internal=50, External=150)											

Number of Credits registered for : 224 Aggregate Marks Secured for best: 216

Aggregate Marks Secured 3681 OUT OF 5250 (70.11%)

Date of Issue: June 9, 2017

(see overleaf for Rules concerned to award of class)

A indicates 'ABSENT'



(\*Courses registered but not counted for calculation of aggregate)

*S. Tara kalyani*

CONTROLLER OF EXAMINATIONS



## **AWARD OF CLASS (FOR ALL COURSES)**

1st Class with Distinction	:	69.5% or more
1st Class	:	Below 69.5% but not less than 59.5%
2nd Class	:	Below 59.5% but not less than 49.5%
Pass Class	:	Below 49.5% but not less than 40%

- Note : (i) A Student shall be deemed to have satisfied the minimum academic requirements and earned the credits allotted to each theory/practical/design/ drawing subject or project (a) if he/she secures not less than 35% of marks in the end examination and a minimum of 40% of marks in the sum of total of the internal evaluation and the end examination taken together for UG courses (B.Tech/B.Pharm) and (b) if he/she secures not less than 40% of marks in the end examination and a minimum of 50% of marks in the sum of total of the internal evaluation and the end examination taken together for PG courses (M.Tech/M.Pharm./Pharm.D./MCA/MBA/MSc).
- (ii) For lateral entry students, the UG course is of three years duration and the students are directly admitted into II year of the four year UG course.

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**HYDERABAD - 500 085, TELANGANA STATE, INDIA.**

College: WJ - G N I T C, KHANAPUR

31225023083



SI. No. PC 00924333



**PROVISIONAL CERTIFICATE**

21225023121

HT No: 13WJ1A04AZ

This is to certify that Mr. TOLETI SAI SATYA PRAKASH

son of Mr. TOLETI SUBBA RAO

passed B. Tech. (ELECTRONICS & COMMUNICATION ENGINEERING) degree

examination of this University, held in May, 2017 and that he was placed in FIRST CLASS WITH DISTINCTION.

He has satisfied all the requirements for the award of the degree.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY  
HYDERABAD - T.S.

*S. Tara kaly*

Controller of Examinations

*M. S. Reddy*  
REGISTRAR

Date: June 9, 2017

